

Position: NoC - Design Verification Engineer

Job Overview

Job Title: NoC – Design Verification Engineer

Location: Hyderabad

Experience: 5–12 Years (Only from Product-Based Companies)

Job Type: Full-Time

Salary: Best in the Industry

Job Overview:

We are seeking an experienced NoC – Design Verification Engineer to join our team in Hyderabad. The ideal candidate will have strong expertise in Network on Chip (NoC) and proven experience in design verification within a product-based company environment.

Key Responsibilities:

- Develop and execute verification plans for NoC architectures and designs.
- Create, maintain, and enhance test benches, verification environments, and automation scripts.
- Collaborate with architecture, design, and validation teams to ensure high-quality deliverables.
- Analyze and debug complex design and verification issues.

Required Skills:

- Strong experience in NoC (Network on Chip) architecture and protocols.
- Hands-on expertise in design verification methodologies (UVM/OVM).
- Solid understanding of SoC/ASIC design flows.
- Proficient in SystemVerilog and verification tools.

Eligibility: Only candidates from product-based companies will be considered.



www.keninfo.tech



Jobs@Keninfo.tech



+91 8884433033

#1121, 2nd Floor, 4th Cross, Ananthnagar Phase 2, E-City Phase 2, Bangalore, Karnataka – 560100

Head Office: KEN Infotech Ltd 40 Campbell Street, Glasgow, G20 OPD, Scotland, UK

© 2025 KEN Infotech Ltd. All rights reserved. | KEN Confidential