Advanced FPGA Prototyping

The MPS4 board is an FPGA prototyping platform, supporting FPGA implementations of Arm Corstone subsystems, including Arm Cortex-A, Cortex-R, Cortex-M processors, and other Arm IP products.

Features and Benefits

High Performance

A high-performance AMD Virtex UltraScale+ XCVU13P FPGA, with increased capacity over its predecessors. A SODIMM connector and a large amount of memory means you can implement designs as complex as Cortex-A-based systems running Linux applications.

Highly Versatile

A wide array of expansion connectors, peripherals, and ports help accelerate the development of many different applications. For example, PCIe, USB 3.0, CSI, DSI, HDMI, and UARTs.

Debug Support

A range of debug connectors supporting Arm CoreSight debug using Arm DS, Arm Keil and other debug software. Includes Arm Keil ULINKplus debug adapter.

FPGA Implementations

Enables prototyping of SoC designs using Arm FPGA implementations of Arm Corstone subsystems, including Cortex-A, Cortex-R, Cortex-M processors, and other Arm IP products.

Use Cases

Full Lifecycle Development

Device Prototyping

Develop and debug complex SoCs on an evaluation board prior to implementing the optimized design on silicon.

Firmware Development

Develop and debug firmware such as low-level device drivers before you get the final silicon.

Application Level Design

Develop and optimize higher-level code, such as the operating system and applications, so they run within specified parameters.

