



## True8DIGIT Workshop

15 May 2026

### Programme

All times are CEST (Brussels time)

Time	Title	Presenter
10:00 hrs	The True8DIGIT project – objectives, work programme and outputs	Oliver Power, NSAI
10:20 hrs	Precision ADCs – current state-of-the art	Nikolai Beev, CERN and
10:50 hrs	A bridge for measuring distortion of components at the 170 dB level	Rado Lapuh, Left Right s.p.
11:20 hrs	Coffee Break	
11:40 hrs	Characterisation of components for use in precision integrating ADCs	Vitor Cabral, IPQ
12:00 hrs	Measurement of charge injection of electronic Switches	Paolo Durandetto, INRIM
12:20 hrs	Design, fabrication and testing of an ultra-clean power supply with continuous output	Jan Kucera, CMI
12:40 hrs	Lunch	
13:30 hrs	Ultra-low jitter timing platform for high-precision composite ADCs	Bruno Trinchera, INRIM
13:40	Ultra-low distortion sinewaves for ADC testing	Rado Lapuh, Left Right s.p.
14:00	Recent improvements in multi-tone testing of ADCs	R. A Belcher, Signal Conversion Ltd.
14:20	Discussion Forum	
15:00	Close of Workshop	