



# 5th IEEE International Conference on VLSI Systems, Architecture, Technology and Applications (VLSI SATA) 22 – 24 May 2025

**Venue:** Amrita Vishwa Vidyapeetham  
Amrita School of Engineering  
Kasavanahalli, Bengaluru (India), 560035



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## Program Schedule

### Day 0: (22-05-2025), Thursday

Time	Events		
8.30AM - 9.30AM	Registration		
	<b>Tutorial-1 (Krishna Hall)</b>	<b>Tutorial-2 (Rama Hall)</b>	<b>Workshop (Rama Hall)</b>
	Challenges of Digital Design Verification	Physical Verification and Validation of Digital Analog and Mixed-Signal Designs	
9.30AM - 12.30PM (Tea Break Included)	<u>Speaker:</u> <b>Dr Ravikumar C P</b> , Former Director of Technical Talent Development Texas Instrument (India)	<u>Speakers:</u> <b>Ms Leya Wels</b> , Project Engineer at C-DAC Bangalore <b>Dr Pranose J. Edavoor</b> , Sr Digital Design Engineer, C-DAC Bangalore	
12.30PM- 1.30PM	<b>Lunch (Akshaya Hall)</b>		
1.30PM- 4.30PM (Tea Break Included)	<b>Tutorial -1 Cont'd...</b>  <b>[Participants to bring their own Laptop]</b>		Designing Low-Dropout Regulators: Fundamentals to Advanced Techniques  <u>Speakers:</u> <b>Mr. Soham Lakhote</b> <b>Mr. Koteswar Doddi</b> <b>Mr. Rahul Reji</b> Scholar, IISc

### Day 1 (23-05-2025), Friday

Time	Event
8.30AM – 9.30AM	Registration
9.30AM – 10.30AM	<b>Inauguration (Krishna Hall)</b>
10.30AM – 11.00AM	<b>Networking Tea</b>
	<b>Keynote</b>
11.00 AM– 11.45AM	Title: <b>Qubit-Size Low-Power Cryo-CMOS ICs for Monolithic Quantum Processors</b> Speaker: <i>Professor --Domenico Zito</i> <i>AGH University of Science and Technology, Krakow, Poland</i>

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11.45AM – 12.30PM	<b>Invited Talk</b>		
	Title: <b>Trends in VLSI : Past, Present and Future</b> Speaker <i>Girishankar Gurumurthy</i> , <i>Deputy Director, Compute and AI division, MediaTek Bangalore</i>		
	<b>Networking Lunch</b> (Akshaya Hall)		
12.30 PM-1.30PM			
1.30PM- 2.50PM	<b>Paper Presentation</b>		
	Krishna hall	Rama hall	Valmiki hall
	Paper IDs: four papers 72, 89,101,109 SC: Dr. Sarfraz Hussain Dr Jitendra Bahadur	Paper IDs: four papers 29, 176, 354, 358 SC: Dr. Javed GS Dr Kamatchi S	Paper IDs: four papers 97, 195, 196, 197 SC: Dr Kiran Gupta Dr Sarda Sharma
2.50PM – 3.45PM	<b>Panel Discussion</b>		
3.45PM – 4.00PM	Title: <b>Analog Computing for AI</b> Panellist: <b>Prabhavathi P</b> , CEO & Co founder, Skillsoc Technologies LLP, Bangalore, India Dr Javed GS, Intel Professor Domenico Zito, Poland Danish Sheikh, Manager Analog IP/CAD Methodology, Western Digital.		
	<b>Tea Break</b>		
	<b>Paper Presentation</b>		
4.00PM – 5.20 PM	Krishna Hall	Rama Hall	Valmiki Hall
	Paper IDs: Three papers 125, 143, 188 SC: Dr. Sarfraz Hussain Dr Jitendra Bahadur	Paper IDs: Three papers 189, 194, 202 SC: Dr Javed GS Dr Sarda Sharma	Paper IDs: Three papers 107, 118, 144 Dr Kiran Gupta Dr Kamatchi S

## Day 2 (24-05-2025), Saturday

Time	Event		
9.00AM - 10.30AM	<b>Paper Presentation</b>		
	Krishna Hall	Rama Hall	Valmiki Hall
	Paper IDs: 4 papers 108, 276, 314, 353 SC: Dr. Uma S V Dr Chinthala R	Paper IDs: 4 papers 247, 318, 321, 330 SC: Sajal Mittal Dr Paramasivam C	Paper IDs: 4papers 142, 224, 259, 331 SC: Dr. Shashidhara K S Subhra Prakash Giri
10.30AM-11.00AM	<b>Tea Break</b>		



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11.00AM-11.45AM	<b>Invited Talk</b>		
	<b>Title: Open-source Semiconductor Ecosystem</b> <b>Speaker: Aloke Das</b> <i>Director, Lab and Lectures</i>		
11.45AM-12.30PM	<b>Keynote Talk</b> (ONLINE)		
	<b>Title: Trends on Electronic Design Automation.</b> <b>Speaker: Professor, Ricardo Reis</b> <i>Instituto de Informática, Universidade Federal do Rio Grande do Sul, at Porto Alegre, Brazil.</i>		
12.30PM – 1.00PM	<b>Invited Talk</b>		
	<b>Title: VLSI Technology: Bolstering Communication Systems</b> <b>Speaker: Dr Alok Nath De</b> <i>Founder and CEO - TechCrafter.cps, Formerly CTO &amp; CVP - Samsung</i>		
1.00PM – 2.00PM	<b>Networking Lunch</b>		
2.00PM-3.30PM	<b>Paper Presentation</b>		
	Krishna Hall <b>Paper IDs: three papers</b> 268, 301, 343, 359 <b>SC: Dr. Uma S V</b> Vignesh G	Rama Hall <b>Paper IDs: three papers</b> 181, 270, 345, 351 <b>SC: Sajal Mittal</b> Dr Paramasivam C	Valmiki Hall <b>Paper IDs: three papers</b> 35, 102, 277, 348 <b>SC: Dr. Shashidhara K S</b> Subhra Prakash Giri
3.30PM-4.00PM	<b>Valedictory</b>		
4.00PM- 4.30PM	<b>High Tea</b>		

Please contact program chair/local arrangement chair for any query. (If your paper is missing, kindly reach out to us but don't worry, it can be adjusted on your arrival also). Meet immediately on your arrival.

Few rooms/accommodation is available within the campus at discounted rate. Reach out to Local Arrangement chair ([ganapathi\\_hegde@blr.amrita.edu](mailto:ganapathi_hegde@blr.amrita.edu)) for query.

For others – [vlsisata@gmail.com](mailto:vlsisata@gmail.com)